

PRODUCT SPECIFICATION

6252M-PUB

Wi-Fi Dual-band 2T2R 11ax +Bluetooth 5.2

Combo Module

Version:v1.1



6252M-PUB Module Datasheet

| Ordering Information | Part NO. | Description |
|----------------------|---------------|---|
| | FG6252MPUB-00 | RTL8852BE-CG, a/b/g/n/ac/ax, Wi-Fi+BT5.2, 2T2R, 22X30mm, PCIE+USB, PCB Version V1.0 |

Customer: _____

Customer P/N: _____

Signature: _____

Date: _____

Office: 14th floor, Block B, phoenix zhigu, Xixiang Street, Baoan District, Shenzhen

Factory: NO.8, Litong RD., Liuyang Economic & Technical Development Zone, Changsha, CHINA

TEL:+86-755-2955-8186

Website:www.fn-link.com

CONTENTS

| | |
|---|-----------|
| 1. General Description | 5 |
| 1.1 Introduction | 5 |
| 1.2 Description | 5 |
| 2. Features | 6 |
| 3. Block Diagram | 6 |
| 4. General Specification | 7 |
| 4.1 2.4GHz RF Specification | 7 |
| 4.2 5GHz RF Specification | 8 |
| 4.2 Bluetooth Specification | 9 |
| 5. ID setting information | 10 |
| 6. Pin Definition | 11 |
| 6.1 Pin Outline | 11 |
| 6.2 Pin Definition details | 11 |
| 7. Electrical Specifications | 14 |
| 7.1 Power Supply DC Characteristics | 14 |
| 7.2 Power Consumption | 14 |
| 7.3 Interface Circuit time series | 15 |
| 7.3.1 PCIe Bus during Power On Sequence | 15 |
| 7.3.2 PCIe PERST# Timing Sequence | 15 |
| 7.3.3 Power Off Sequence | 16 |
| 7.3.4 BT_DIS Timing Sequence | 16 |
| 7.3.5 Platform state transitions | 16 |
| 8. Size reference | 17 |
| 8.1 Module Picture | 17 |
| 8.2 Marking Description | 17 |
| 8.3 Physical Dimensions | 18 |
| 9. The Key Material List | 19 |
| 10. Reference Design | 19 |
| 10.1 Reference design | 19 |
| 10.2 Connector Specification | 20 |
| 11. Recommended Reflow Profile | 21 |
| 12. RoHS compliance | 21 |
| 13. Package | 22 |
| 13.1 Tray | 22 |
| 14. Moisture sensitivity | 23 |

Revision History

| Version | Date | Contents of Revision Change | Draft | Checked | Approved |
|---------|------------|--|-------|---------|----------|
| V1.0 | 2021/09/26 | Initial Release | Tzq | Tzq | Qjp |
| V1.1 | 2022/05/24 | Update Specification Format Change RF power tolerance to ±2dBm Add module physical drawing Update packaging information | Fc | Tzq | Qjp |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | | |

1. General Description

1.1 Introduction

The fn-link6252M-PUB is a highly integrated single-chip that support 2-stream 802.11ax solutions with Multi-user MIMO (Multiple-Input, Multiple-Output) with Wireless LAN (WLAN) PCI Express network interface controller with integrated Bluetooth 5 USB interface controller. It combines a WLAN MAC, a 2T2R capable WLAN baseband, and RF in a single chip. The RTL8852BE provides a complete solution for a high-performance integrated wireless and Bluetooth device.

1.2 Description

| | |
|-----------------------|--|
| Model Name | 6252M-PUB |
| Product Description | Support Wi-Fi/Bluetooth functionalities |
| Dimension | L x W x H: 22x 30x 2.2 (typical) mm |
| Wi-Fi Interface | Support PCIe |
| BT Interface | USB |
| OS supported | Android /Linux/ Win CE /iOS /XP/WIN7/WIN10 |
| Operating temperature | 0°C to 70°C |
| Storage temperature | -55°C to 85°C |

2. Features

General Features

- IEEE 802.11a/b/g/n/ac/ax compatible WLAN
- Supports 20/40MHz at 2.4GHz and supports 20/40/80MHz at 5GHz.
- Supports 802.11ac 2x2, Wave-2 compliant with RX MU-MIMO
- Complete 802.11n MIMO solution for 2.4GHz and 5GHz band

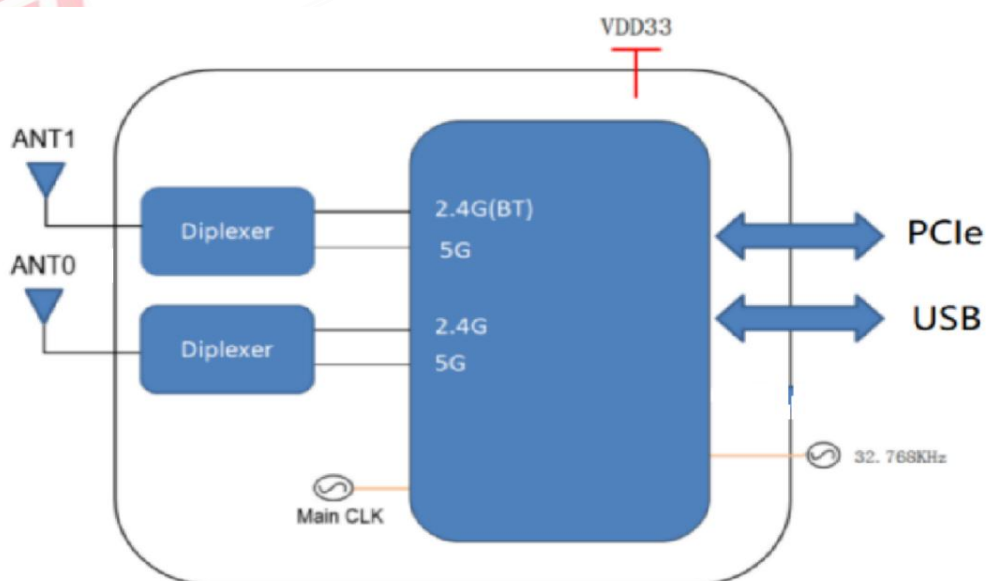
WLAN Interface

- Supports low power PCIe(Base Specification Revision 1.1) interface for WLAN and USB(2.0 FS-mode)

Bluetooth Features

- Supports Bluetooth 5.0 system
- Compatible with Bluetooth v2.1+EDR
- Dual Mode support: Simultaneous LE and BR/EDR
- Enhanced BT/Wi-Fi Coexistence Control to improve transmission quality in different profiles
- Supports Bluetooth for class1, class2 and class3 power level transmissions without requiring an external PA
- Integrated 32K oscillator for power management

3. Block Diagram



4. General Specification

4.1 2.4GHz RF Specification

| Feature | Description | |
|--|--|----------------|
| WLAN Standard | IEEE 802.11 b/g/n/ac/ax Wi-Fi compliant | |
| Frequency Range | 2.400 GHz ~ 2.497 GHz (2.4 GHz ISM Band) | |
| Number of Channels | 2.4GHz: Ch1 ~ Ch14 | |
| Test Items | Typical Value | EVM |
| Output Power | 802.11b /11Mbps: 19dBm ± 2 dB | EVM ≤ -9dB |
| | 802.11g /54Mbps: 18dBm ± 2 dB | EVM ≤ -25dB |
| | 802.11n /MCS7: 17dBm ± 2 dB | EVM ≤ -28dB |
| | 802.11ac VHT20 MCS8: 16dBm ± 2 dB | EVM ≤ -30dB |
| | 802.11ac VHT40 MCS9: 15dBm ± 2 dB | EVM ≤ -32dB |
| | 802.11ax HE20 MCS11: 13dBm ± 2 dB | EVM ≤ -35dB |
| | 802.11ax HE40 MCS11: 13dBm ± 2 dB | EVM ≤ -35dB |
| Spectrum Mask | Meet with IEEE standard | |
| Freq. Tolerance | ± 20ppm | |
| Test Items | TYP Test Value | Standard Value |
| SISO Receive Sensitivity (11b,20MHz) @8% PER | - 1Mbps @ -94 dBm | ≤-83 dBm |
| | - 11Mbps @ -85 dBm | ≤-76 dBm |
| SISO Receive Sensitivity (11g,20MHz) @10% PER | - 6Mbps @ -90 dBm | ≤-85 dBm |
| | - 54Mbps @ -71 dBm | ≤-68 dBm |
| SISO Receive Sensitivity (11n,20MHz) @10% PER | - MCS=0 @ -90 dBm | ≤-85 dBm |
| | - MCS=7 @ -69 dBm | ≤-67 dBm |
| SISO Receive Sensitivity (11n ,40MHz) @10% PER | - MCS=0 @ -87 dBm | ≤-82 dBm |
| | - MCS=7 @ -66 dBm | ≤-64 dBm |
| SISO Receive Sensitivity (11ac,20MHz) @10% PER | - MCS=0 @ -90 dBm | ≤ -82 dBm |
| | - MCS=8 @ -64 dBm | ≤ -60 dBm |
| SISO Receive Sensitivity (11ac ,40MHz) @10% PER | - MCS=0 @ -87 dBm | ≤ -79 dBm |
| | - MCS=9 @ -59 dBm | ≤ -55 dBm |
| SISO Receive Sensitivity (11ax,20MHz) @10% PER | - MCS=0 @ -90 dBm | ≤-74 dBm |
| | - MCS=11 @ -60 dBm | ≤-52 dBm |
| SISO Receive Sensitivity (11ax ,40MHz) @10% PER | - MCS=0 @ -87 dBm | ≤-71 dBm |
| | - MCS=11 @ -57 dBm | ≤-49 dBm |
| Maximum Input Level | 802.11b : -10 dBm | |
| | 802.11g/n : -20 dBm | |

| | |
|-------------------|---------------------------------------|
| Antenna Reference | Small antennas with 0~2 dBi peak gain |
|-------------------|---------------------------------------|

4.2 5GHz RF Specification

Conditions : VBAT=3.3V ; VDDIO=3.3V ; Temp:25°C

| Feature | Description | |
|---|---|----------------|
| WLAN Standard | IEEE 802.11a/n/ac/ax 2x2, Wi-Fi compliant | |
| Frequency Range | 4.900 GHz ~ 5.845 GHz (5.0 GHz ISM Band) | |
| Number of Channels | 5.0GHz: Please see the table1 | |
| Test Items | Typical Value | EVM |
| Output Power ² | 802.11a /54Mbps: 18 dBm ± 2 dB | EVM ≤ -25dB |
| | 802.11n /MCS7: 17 dBm ± 2 dB | EVM ≤ -28dB |
| | 802.11ac VHT20 MCS8: 16 dBm ± 2 dB | EVM ≤ -30dB |
| | 802.11ac VHT40 MCS9: 15 dBm ± 2 dB | EVM ≤ -32dB |
| | 802.11ac VHT80 MCS9: 15 dBm ± 2 dB | EVM ≤ -32dB |
| | 802.11ax HE20 MCS11: 13 dBm ± 2 dB | EVM ≤ -35dB |
| | 802.11ax HE40 MCS11: 13 dBm ± 2 dB | EVM ≤ -35dB |
| | 802.11ax HE80 MCS11: 13 dBm ± 2 dB | EVM ≤ -35dB |
| Test Items | Test Value | Standard Value |
| SISO Receive Sensitivity (11a,20MHz) @10% PER | - 6Mbps @ -90 dBm | ≤-85 |
| | - 54Mbps @ -71 dBm | ≤-68 |
| SISO Receive Sensitivity (11n,20MHz) @10% PER | - MCS=0 @ -90 dBm | ≤-85 |
| | - MCS=7 @ -69 dBm | ≤-67 |
| SISO Receive Sensitivity (11n,40MHz) @10% PER | - MCS=0 @ -87 dBm | ≤-82 |
| | - MCS=7 @ -66 dBm | ≤-64 |
| SISO Receive Sensitivity (11ac,20MHz)@10% PER | - MCS=0, NSS1 @ 90 dBm | ≤-82 |
| | - MCS=8, NSS1 @ -64 dBm | ≤-60 |
| SISO Receive Sensitivity (11ac,40MHz) @10% PER | - MCS=0, NSS1 @ -87 dBm | ≤-79 |
| | - MCS=9, NSS1 @ -59 dBm | ≤-55 |
| SISO Receive Sensitivity (11ac,80MHz) @10% PER | - MCS=0, NSS1 @ -84 dBm | ≤-79 |
| | - MCS=9, NSS1 @ -56 dBm | ≤-54 |
| SISO Receive Sensitivity (11ax,20MHz) @10% PER | - MCS=0 @ -90 dBm | ≤-74 |
| | - MCS=11 @ -60 dBm | ≤-52 |
| SISO Receive Sensitivity (11ax,40MHz) @10% PER | - MCS=0 @ -87 dBm | ≤-71 |
| | - MCS=11 @ -57 dBm | ≤-49 |

| | | |
|---|---------------------------------------|------|
| SISO Receive Sensitivity (11ax,80MHz) @10% PER | - MCS=0 @ -84 dBm | ≤-68 |
| | - MCS=11 @ -54 dBm | ≤-46 |
| Maximum Input Level | 802.11a/n: -30 dBm | |
| Antenna Reference | Small antennas with 0~2 dBi peak gain | |

15GHz(20MHz) Channel table

| Band range | Operating Channel Numbers | Channel center frequencies(MHz) |
|-------------------|----------------------------------|--|
| 5180MHz~5240MHz | 36 | 5180 |
| | 40 | 5200 |
| | 44 | 5220 |
| | 48 | 5240 |
| 5260MHz~5320MHz | 52 | 5260 |
| | 56 | 5280 |
| | 60 | 5300 |
| | 64 | 5320 |
| 5550MHz~5700MHz | 100 | 5500 |
| | 104 | 5520 |
| | 108 | 5540 |
| | 112 | 5560 |
| | 116 | 5580 |
| | 120 | 5600 |
| | 124 | 5620 |
| | 128 | 5640 |
| | 132 | 5660 |
| | 136 | 5680 |
| 5745MHz~5825MHz | 140 | 5700 |
| | 149 | 5745 |
| | 153 | 5765 |
| | 157 | 5785 |
| | 161 | 5805 |
| | 165 | 5825 |

4.2 Bluetooth Specification

| Feature | Description |
|------------------------------|------------------------------------|
| General Specification | |
| Bluetooth Standard | Bluetooth V5.2 of 1, 2 and 3 Mbps. |

| | | | |
|---|---------------------------------------|---------------------|-----------------|
| Host Interface | USB | | |
| Antenna Reference | Small antennas with 0~2 dBi peak gain | | |
| Frequency Band | 2402 MHz ~ 2480 MHz | | |
| Number of Channels | 79 channels | | |
| Modulation | GFSK, $\pi/4$ -DQPSK, 8-DPSK | | |
| RF Specification | | | |
| | Min(dBm) | Typical(dBm) | Max(dBm) |
| Output Power | | 5 | |
| Sensitivity @ BER=0.1% for GFSK (1Mbps) | | | -70 |
| Sensitivity @ BER=0.01% for $\pi/4$ -DQPSK (2Mbps) | | | -70 |
| Sensitivity @ BER=0.01% for 8DPSK (3Mbps) | | | -70 |
| Maximum Input Level | GFSK (1Mbps):-20dBm | | |
| | $\pi/4$ -DQPSK (2Mbps) :-20dBm | | |
| | 8DPSK (3Mbps) :-20dBm | | |

5. ID setting information

WI-FI

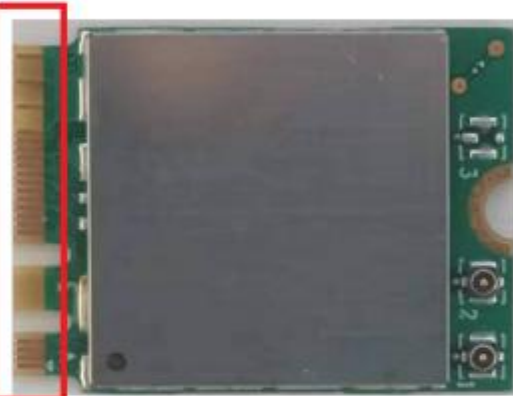
| | |
|------------|---|
| Vendor ID | - |
| Product ID | - |

6. Pin Definition

6.1 Pin Outline

< TOP VIEW >

| PIN | Signal | Signal | PIN |
|-----|----------------|----------|-----|
| 74 | NC | GND10 | 75 |
| 72 | NC | NC | 73 |
| 70 | NC | NC | 71 |
| 68 | NC | GND9 | 69 |
| 66 | NC | NC | 67 |
| 64 | NC | NC | 65 |
| 62 | NC | GND | 63 |
| 60 | NC | NC | 61 |
| 58 | NC | NC | 59 |
| 56 | WL_DIS_N | GND | 57 |
| 54 | BT_DIS_N | PEWAKE0 | 55 |
| 52 | PERST0 | CLKREQ0 | 53 |
| 50 | SUSCLK | GND | 51 |
| 48 | COEX_RXD | REFCLKN0 | 49 |
| 46 | COEX_TXD | REFCLKP0 | 47 |
| 44 | COEX3 | GND | 45 |
| 42 | NC | PETN0 | 43 |
| 40 | NC | PETP0 | 41 |
| 38 | VENDOR DEFINED | GND | 39 |
| 36 | NC | PERN0 | 37 |
| 34 | NC | PERP0 | 35 |
| 32 | NC | GND | 33 |
| 30 | NC | NC | 31 |
| 28 | NC | NC | 29 |
| 26 | NC | NC | 27 |
| 24 | NC | NC | 25 |
| 22 | NC | NC | 23 |
| 20 | NC | NC | 21 |
| 18 | GND | NC | 19 |
| 16 | LED_2# | NC | 17 |
| 14 | NC | NC | 15 |
| 12 | NC | NC | 13 |
| 10 | NC | NC | 11 |
| 8 | NC | NC | 9 |
| 6 | LED_1# | GND | 7 |
| 4 | 3_3V | USB_D- | 5 |
| 2 | 3_3V | USB_D+ | 3 |
| | | GND | 1 |



6.2 Pin Definition details

| NO | Name | Type | Description | Voltage |
|----|--------|------|------------------------------|---------|
| 1 | GND | - | Ground connections | |
| 3 | USB_D+ | I/O | USB differential line for BT | |
| 5 | USB_D- | I/O | | |
| 7 | GND | - | Ground connections | |
| 9 | NC | - | Floating (NC) | |
| 11 | NC | | Floating (NC) | |

| | | | | |
|----|----------|---|---|------|
| 13 | NC | | Floating (NC) | |
| 15 | NC | | Floating (NC) | |
| 17 | NC | - | Floating (NC) | |
| 19 | NC | - | Floating (NC) | |
| 21 | NC | - | Floating (NC) | |
| 23 | NC | - | Floating (NC) | |
| 25 | NC | | Floating (NC) | |
| 27 | NC | | Floating (NC) | |
| 29 | NC | | Floating (NC) | |
| 31 | NC | | Floating (NC) | |
| 33 | GND | - | Ground connections | |
| 35 | PERP0 | I | PCIe RX differential signals | |
| 37 | PERN0 | I | | |
| 39 | GND | - | Ground connections | |
| 41 | PETP0 | O | PCIe TX differential signals | |
| 43 | PETN0 | O | | |
| 45 | GND | - | Ground connections | |
| 47 | REFCLKP0 | I | PCIe clock differential input signal | |
| 49 | REFCLKN0 | I | | |
| 51 | GND | | Ground connections | |
| 53 | CLKREQ0 | O | PCIe reference clock request signal, open drain, active low | 3.3V |
| 55 | PEWAKE0 | O | PCIe wake up host, open drain, active low | 3.3V |
| 57 | GND | - | Ground connections | |
| 59 | NC | - | Floating (NC) | |
| 61 | NC | - | Floating (NC) | |
| 63 | GND | - | Ground connections | |
| 65 | NC | - | Floating (NC) | |
| 67 | NC | - | Floating (NC) | |
| 69 | GND9 | - | Ground connections | |
| 71 | NC | - | Floating (NC) | |
| 73 | NC | - | Floating (NC) | |
| 75 | GND10 | - | Ground connections | |

Bottom side

| NO | Name | Type | Description | Voltage |
|----|------------------|------|---|---------|
| 2 | 3_3V | P | Power supply | 3.3V |
| 4 | 3_3V | P | Power supply | 3.3V |
| 6 | LED_1# | O | WLAN LED signal | 3.3V |
| 8 | PCM_CLK | I/O | general perpose input | |
| 10 | PCM_SYNC | I/O | general perpose input | |
| 12 | PCM_OUT | I/O | general perpose input | |
| 14 | PCM_IN | I/O | general perpose input | |
| 16 | LED_2# | O | BT LED signal | 3.3V |
| 18 | GND | - | Ground connections | |
| 20 | BT_WAKE_HOST_Ant | O | Bluetooth device to wake-up HOST | 3.3V |
| 22 | NC | - | Floating (NC) | |
| 24 | NC | | Floating (NC) | |
| 26 | NC | | Floating (NC) | |
| 28 | NC | | Floating (NC) | |
| 30 | NC | | Floating (NC) | |
| 32 | NC | | Floating (NC) | |
| 34 | NC | - | Floating (NC) | |
| 36 | NC | - | Floating (NC) | |
| 38 | VENDOR DEFINED | - | Host wake BT. No function, please don't connect to this pin. | |
| 40 | NC | - | Floating (NC) | |
| 42 | NC | - | Floating (NC) | |
| 44 | COEX3 | I/O | LTE coexistence signal | 3.3V |
| 46 | COEX_TXD | O | LTE coexistence signal | 3.3V |
| 48 | COEX_RXD | I | LTE coexistence signal | 3.3V |
| 50 | SUSCLK | I | Sleep clock input | 3.3V |
| 52 | PERST0 | I | PCIe reset signal, active low | 3.3V |
| 54 | BT_DIS_N | I | Bluetooth enable signal, pull low to disable BT function, default high. | 3.3V |
| 56 | WL_DIS_N | I | WLAN enable signal, pull low to disable BT function, default high. | 3.3V |
| 58 | NC | - | Floating (NC) | |
| 60 | NC | - | Floating (NC) | |

| | | | | |
|----|----|---|---------------|--|
| 62 | NC | - | Floating (NC) | |
| 64 | NC | - | Floating (NC) | |
| 66 | NC | - | Floating (NC) | |
| 68 | NC | - | Floating (NC) | |
| 70 | NC | - | Floating (NC) | |
| 72 | NC | - | Floating (NC) | |
| 74 | NC | - | Floating (NC) | |

P:POWER I:INPUT O:OUTPUT

7. Electrical Specifications

7.1 Power Supply DC Characteristics

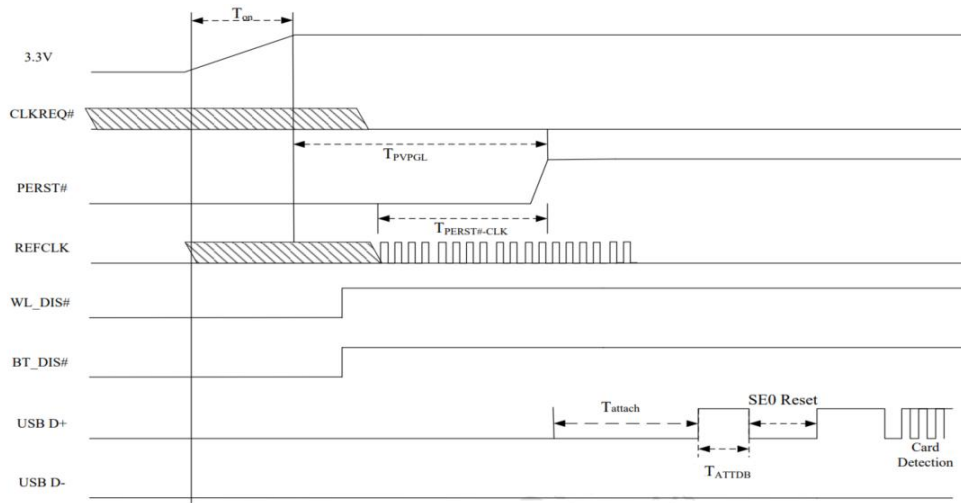
| | MIN | TYP | MAX | Unit |
|-----------------------|-----|-----|-----|-------|
| Operating Temperature | 0 | 25 | 70 | deg.C |
| VDD33 | 3.0 | 3.3 | 3.6 | V |

7.2 Power Consumption

| Power Consumption | VDD33 = 3.3V(Unit:mA) | |
|-------------------|-----------------------|---|
| | Wi-Fi on Mode | - |
| TX (2.4G 1M) | - | |
| TX (2.4G HT40) | - | |
| RX (2.4G HT40) | - | |
| TX (5G 6M) | - | |
| TX (5G vHT80) | - | |
| RX (5G vHT80) | - | |
| BT on | - | |
| BT Hopping | - | |
| BT TX | - | |
| BT RX | - | |

7.3 Interface Circuit time series

7.3.1 PCIe Bus during Power On Sequence



T_{on}: The main power ramp up duration

T_{PV PGL}: Power valid to PERST# input inactive

T_{PERST#-CLK}: Reference clock stable before PERST# inactive

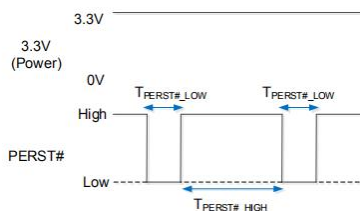
T_{attach}: The interval to turn on BT after PERST# de-asserted

T_{ATTDB}: the debounce interval with a minimal duration of 100ms that provided by the USB system Software

T_{SE0 Reset}: USB host send SE0 Reset duration

| Symbol | Unit | Min | Typical | Max |
|-------------------------------|------|---|---------|-----|
| T_{on} | ms | 0.5 | 1.5 | 5 |
| T_{PV PGL} | ms | Implementation specific; recommended 50ms | | -- |
| T_{PERST#-CLK} | us | 100 | -- | -- |
| T_{attach} | ms | 0.5 | 2 | 5 |
| T_{ATTDB} | ms | 100 | -- | -- |
| T_{SE0 Reset} | ms | 10 | -- | -- |

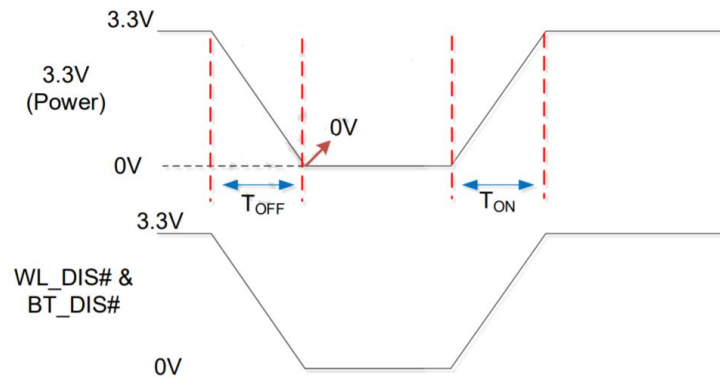
7.3.2 PCIe PERST# Timing Sequence



RTL8821CE-CG PCIe PERST# Timing Parameters

| | Min | Typical | Max | Unit | Description |
|--------------------------|-----|---------|-----|------|----------------------|
| T _{PERST#_LOW} | 6 | 10 | X | ms | PERST# low duration |
| T _{PERST#_HIGH} | 400 | 500 | X | ms | PERST# high duration |

7.3.3 Power Off Sequence

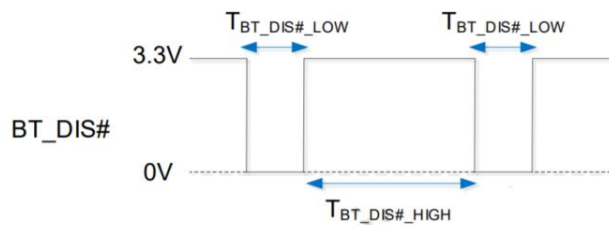


RTL8822CE-CG Power Off Timing Parameters

| Symbol | Min | Typical | Max | Unit | Description |
|------------------|-------|---------|-----|------|--|
| T _{OFF} | 1.5ms | -- | -- | ms | Measure point start on 100% Measure point end on 0% (must be 0V) |
| T _{ON} | 0.5 | 1.5 | 5 | ms | Measure point start on 0% (must be 0V) Measure point end on 100% |

Note: If BT_DIS# can't connect to the same power source with 3.3V, it need to be de-asserted before PERST# with 100ms in power on sequence.

7.3.4 BT_DIS Timing Sequence



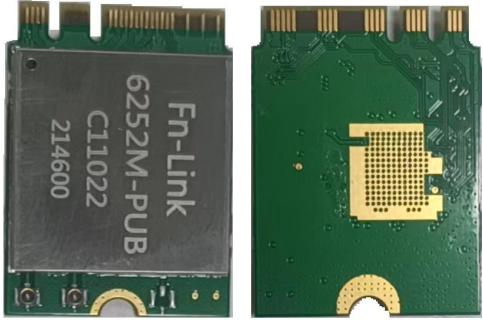
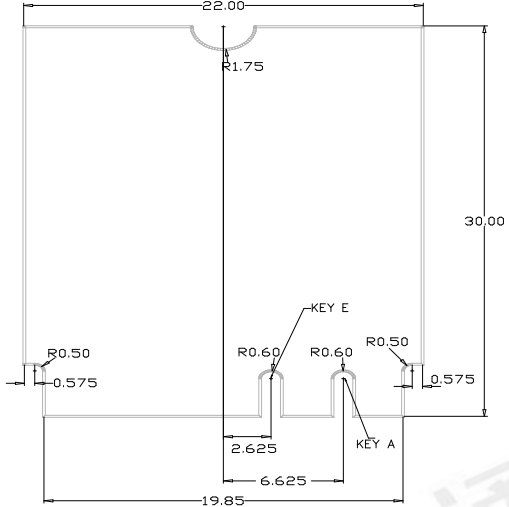

| | Min | Typical | Max | Unit | Description |
|--------------|-----|---------|-----|------|-----------------------|
| BT_DIS#_LOW | 200 | -- | -- | ms | BT_DIS# low duration |
| BT_DIS#_HIGH | 500 | -- | -- | ms | BT_DIS# high duration |

7.3.5 Platform state transitions

| 3.3V Power range | 3.3V Ripple | 3.3V Noise | Rise time | |
|------------------|--|------------|-----------|-----|
| | | | Min | Max |
| +/-0.165V | 300mVpp @ switching frequency > 100KHz | | 0.5ms | 5ms |

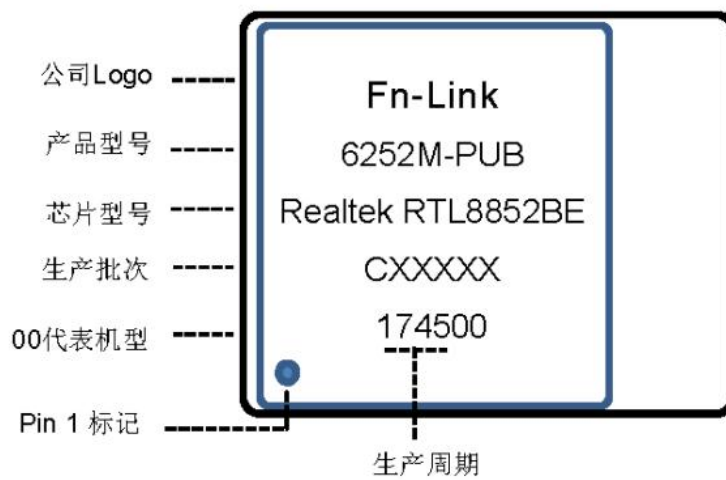
8. Size reference

8.1 Module Picture

| | |
|--|---|
| <p>L x W : 22 x 30 (+0.3/-0.1) mm</p>  |  |
| <p>H: 2.2 (±0.2) mm</p> |  |
| <p>Weight</p> | <p>2.6g</p> |

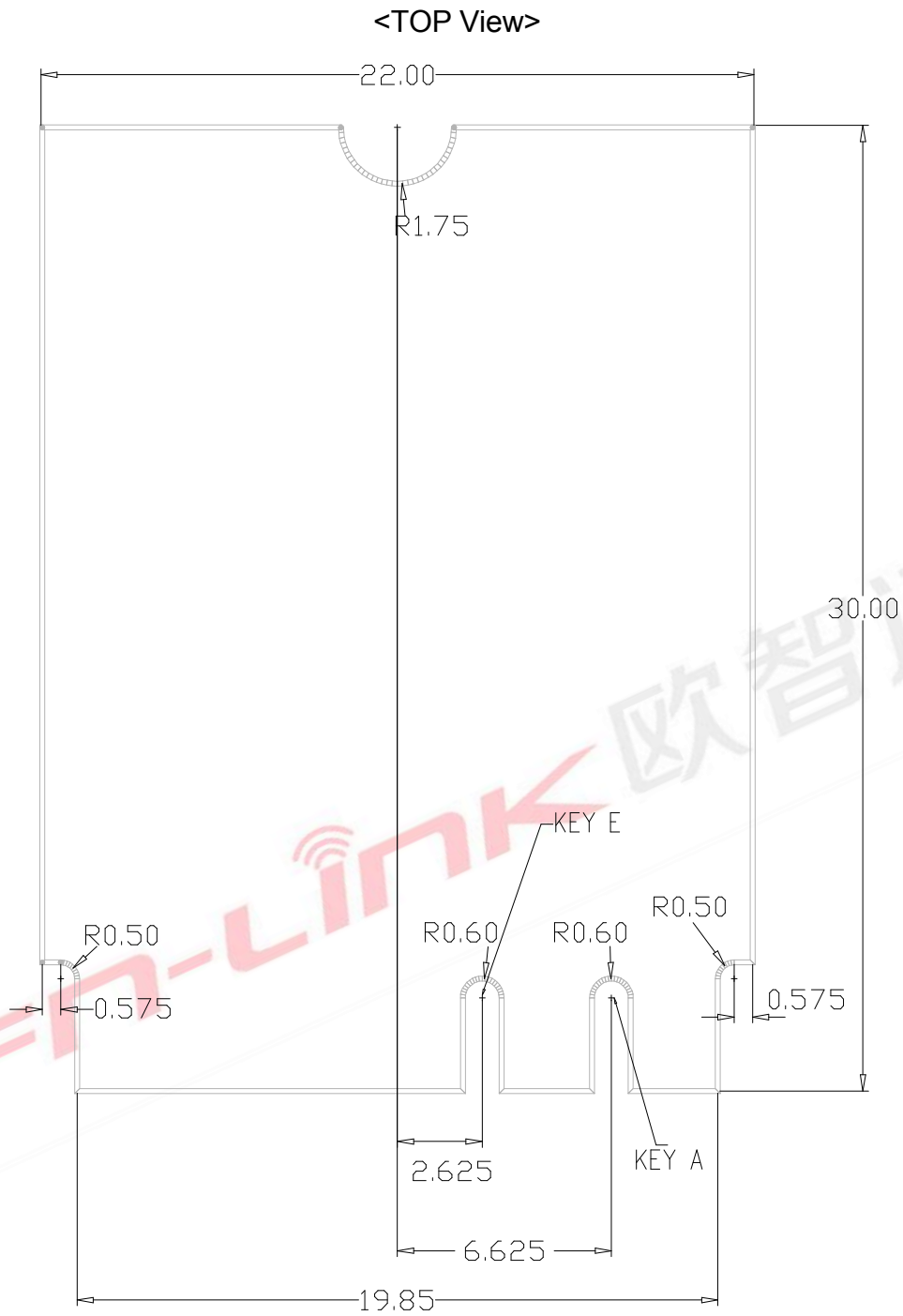
8.2 Marking Description

< TOP VIEW >



模组尺寸: 22x30mm
屏蔽盖尺寸: 21.19x20.98mm

8.3 Physical Dimensions

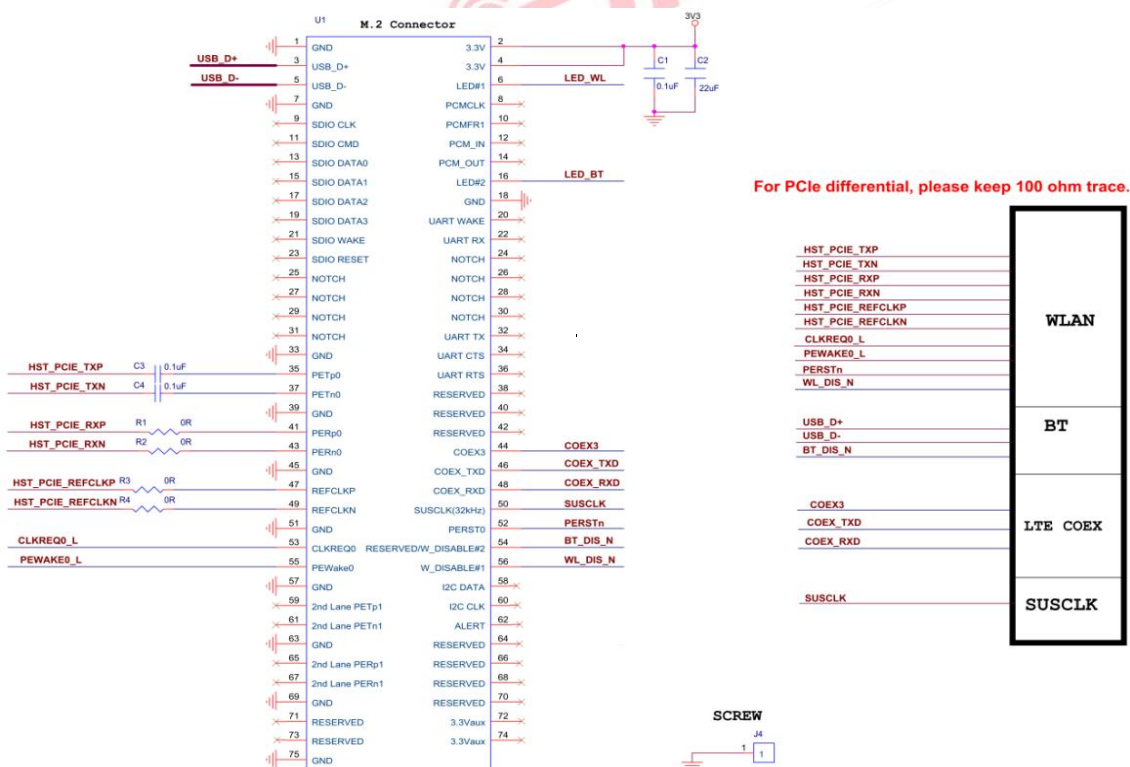


9. The Key Material List

| Item | Part Name | Description | Manufacturer |
|------|-----------|---|--|
| 1 | PCB | 6252M-PUB 22X30X0.8mm TG180 | XY-PCB, GDKX, Sunlord, Piotek, SLPCB, Brain-power, KX-pc |
| 2 | Inductor | 2520 2.2UH ±20%, | Sunlord, Ceaiya, Cenker |
| 3 | Diplexer | 1608 Dual-band, dual-mode 2.4GHz/5GHz WLAN | Glead, Walsin, ACX, Murata, MAG.LAYERS |
| 4 | Crystal | 3225 40MHz 12pF ±10ppm | ECEC, TKD, Hosonic, JWT, TXC |
| 5 | Chipset | RTL8852BE-CG | Realtek |
| 6 | Shielding | 6252M-PUB Shielding cover | Suntech, JLitong |

10. Reference Design

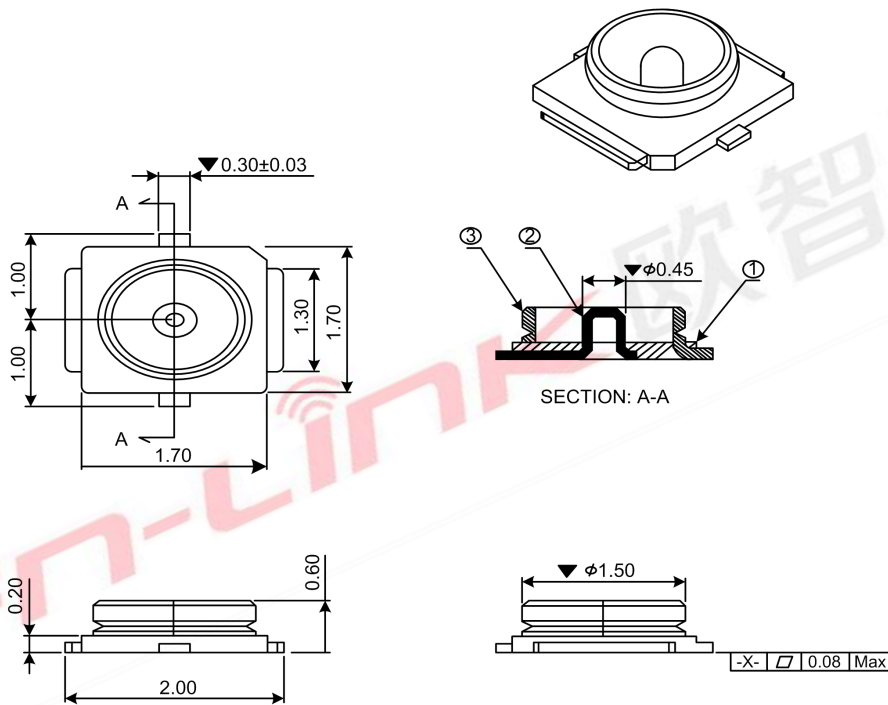
10.1 Reference design



Note: 1. Both of the 2 ANT's are all support 2.4G/5G/BT function.

2. 6252M-PUB antenna port is control by driver if diversity function is enabled.
3. C1, C2 placed close to module side.
4. PCIe differential keep 100 ohm trace.
5. USB differential keep 90 ohm trace.

10.2 Connector Specification

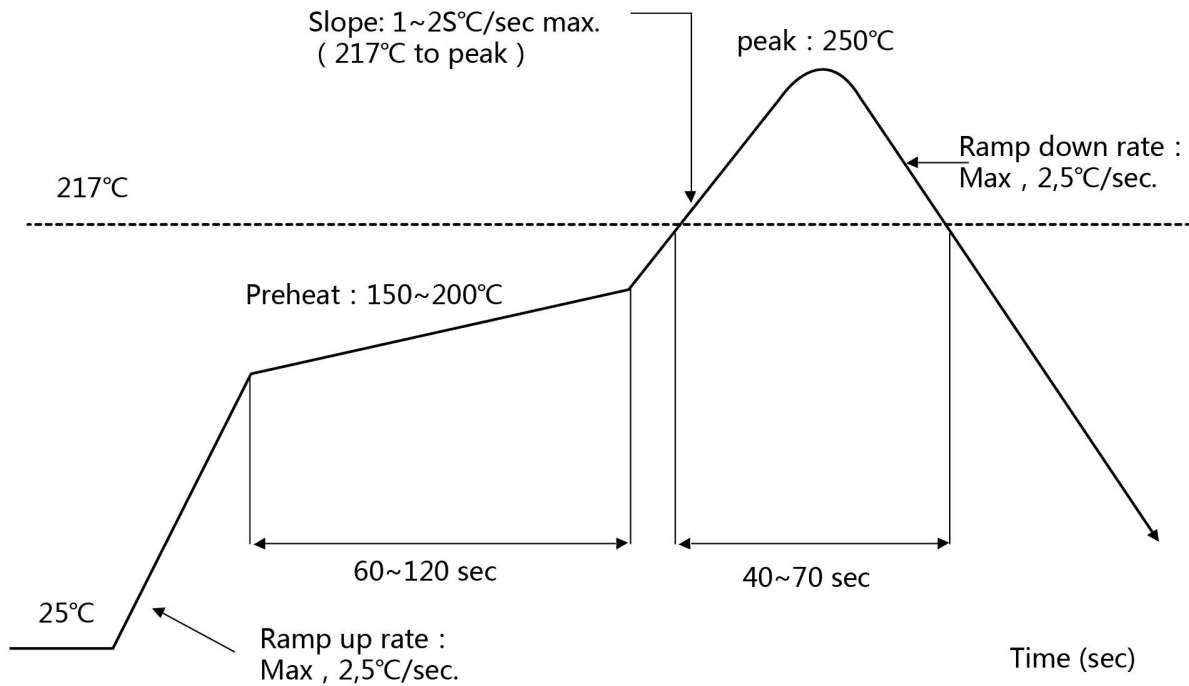


11. Recommended Reflow Profile

Referred to IPC/JEDEC standard.

Peak Temperature : <250°C

Number of Times : ≤2 times



12. RoHS compliance

All hardware components are fully compliant with EU RoHS directive

13. Package

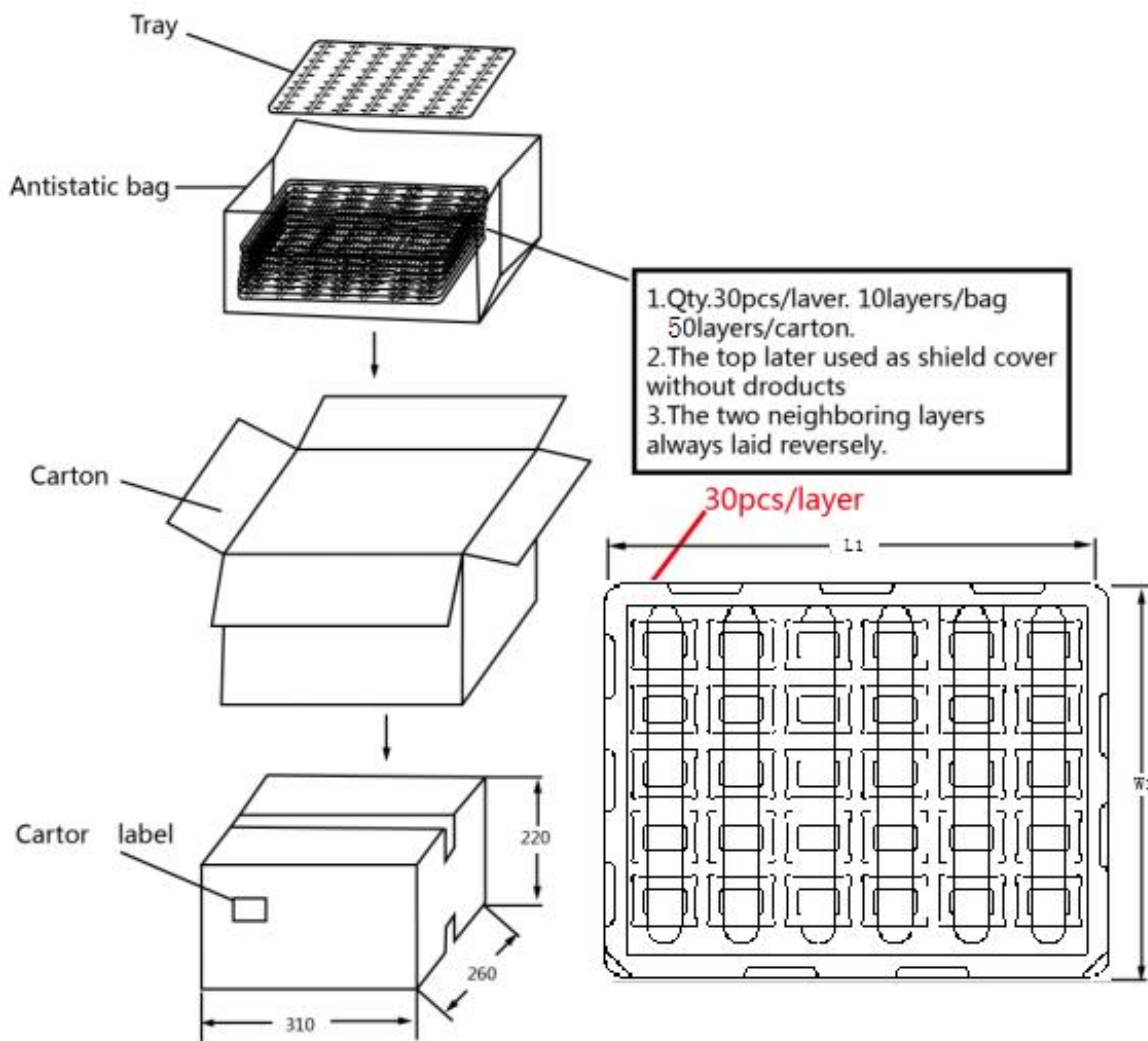
13.1 Tray

Layer size: L250.0*W190.0 mm

Layer material: PVC

Carton size: L310.0*W260.0*H220.0 mm

Carton material: A=A



14. Moisture sensitivity

The Modules is a Moisture Sensitive Device level 3, in according with standard IPC/JEDEC J-STD-020, take care

all the relatives requirements for using this kind of components.

Moreover, the customer has to take care of the following conditions:

- a) Calculated shelf life in sealed bag: 12 months at <math><40^{\circ}\text{C}</math> and <math><90\%</math> relative humidity (RH)
- b) Environmental condition during the production: - c) The maximum time between the opening of the sealed bag and the reflow process must be 168 hours if condition
- d) "IPC/JEDEC J-STD-033A paragraph 5.2" is respected
- e) Baking is required if conditions b) or c) are not respected
- f) Baking is required if the humidity indicator inside the bag indicates 10% RH or more